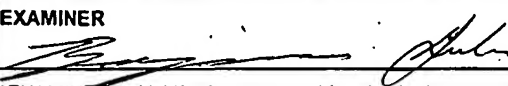


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B.D.		5951678	09.14.1999	Moyer			
B.D.		5860152	01.12.1999	Savkar			
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FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
B.D.		2308470	06.25.1997	UK			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
B.D.		"A Comparison of Full and Partial Predicated Execution Support for LLP Processors" by S. Mahlke et al., Proceedings of the Annual Symposium on Computer Architecture, June 22, 1995					
B.D.		"Parallelization of Loops with Exits on Pipelined Architectures" by P. Tirumalai et al., Proceedings of the Supercomputer Conference, November 12, 1990					
B.D.		"Analysis Techniques for Predicated Code" by R. Johnson et al., Proceedings of the Annual IEEE/ACM Int'l Symposium on Microarchitecture, December 2, 1996					
B.D.		"Conditional Annulling for Superscala Processors" by C.H. Wen et al., Proceedings of the Region Ten Conference (TENCON), October 13, 1993					
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